

**REMARKS**

The specification has been amended to correct errors of a typographical and grammatical nature. Due to the large number of corrections thereto, applicants submit herewith a Substitute Specification, along with a marked-up copy of the original specification for the Examiner's convenience. Applicants submit that the substitute specification includes no new matter. Therefore, entry of the Substitute Specification is respectfully requested.

The abstract and claims have also been amended to correct errors of a typographical and grammatical nature.

Entry of the preliminary amendments and examination of the application is respectfully requested.

To the extent necessary, applicant's petition for an extension of time under 37 CFR 1.136. Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account No. 01-2135 (501.43335X00) and please credit any excess fees to such deposit account.

Respectfully submitted,



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**SUBSTITUTE SPECIFICATION****LIQUID CRYSTAL DISPLAY DEVICE****BACKGROUND OF THE INVENTION**

5       The present invention relates to a liquid crystal display device in which a technique is employed for suppressing the generation of undesired electric fields that can be the cause of image retention, which tends to degrade the image quality of the display device.

In a liquid crystal display device, a liquid crystal layer formed of liquid crystal  
10 molecules is sandwiched between two sheets of insulating substrates, preferably made of glass, and, at the same time, at least a pair of electrodes for applying an electric field to the liquid crystal layer are provided to either one or both of the substrates. In an IPS (in-plane switching) type of liquid crystal display device, all of the above-mentioned electrodes for applying an electric field to the liquid crystal  
15 layer are formed on one substrate, and the pixels are turned on and off (that is, switching is effected) so as to selectively form an electric field having components that are parallel to the substrate surface with respect to the liquid crystal layer in selected pixels.

**20 SUMMARY OF THE INVENTION**

Figs. 14A and 14B are diagrammatic views of the vicinity of one pixel of a liquid crystal display device in which an IPS method is employed, wherein Fig. 14A is a plan view and Fig. 14B is a cross sectional view take along a line A-A' in Fig. 14A. To an inner surface of one substrate (first substrate, usually referred to as the thin

film transistor substrate, since it constitutes a substrate on which thin film transistors are formed, as will be explained hereinafter) SUB1 of a pair of substrates which constitute the liquid crystal display device, the following pixel structure is provided. Then, a second substrate SUB2 (not shown in the drawing) on which color filters and 5 the like are formed is laminated to the first substrate SUB1, and a liquid crystal layer is sandwiched and sealed in a gap defined between both substrates.

On an inner surface of one pixel (also referred to as a unit pixel hereinafter), a plurality of scanning signal lines GL extend in the first direction (referred to as X direction hereinafter) and are arranged in parallel in the second direction (referred to 10 as Y direction hereinafter) which intersects the X direction; a plurality of date signal lines DL extend in the Y direction and are arranged in parallel in the X direction; common signal lines (also referred to as common storage lines) CL are disposed close to the scanning signal lines GL and extend in the X direction, while being arranged in parallel in the Y direction; a plurality of thin film transistors TFT are 15 arranged at intersecting portions of the scanning signal lines GL and the date signal lines DL; pixel electrodes PX are driven by the thin film transistors TFT; and common electrodes CT are connected to the common signal line CL and are alternately arranged with respect to the pixel electrodes PX such that the common electrode CT is arranged close to the pixel electrode PX in the X direction. Thus, unit pixels are 20 formed in regions which are surrounded by the scanning signal lines GL and the data signal lines DL.

As shown in Fig. 14B, which shows a cross section taken along a line A-A' in Fig. 14A, the common signal line CL is formed on the first substrate SUB1, and a data signal line DL and a source electrode SD of the thin film transistor TFT are 25 formed on the common signal line CL by way of a gate insulation layer GI. These

electrodes or wiring are covered with a laminated film which is constituted of an inorganic insulation layer PAS and an organic insulation layer OPAS, and the pixel electrodes PX and the common electrodes CT are formed on the laminated film. Although an orientation film which is brought into contact with a liquid crystal layer 5 (not shown in the drawing) is formed over the pixel electrodes PX and the common electrodes CT, these elements are omitted from the drawing.

The pixel electrodes PX and the common electrodes CT are alternately arranged close to each other in a comb-teeth shape. As shown in Fig. 14B, the pixel electrodes PX are connected to the source electrode SD, which constitutes an output 10 electrode of the thin film transistor TFT, via a through hole SH. As shown in Fig. 14A, the source electrode SD is superposed on the common signal line CL, and a portion which forms the through hole SH projects into the inside of the unit pixel region in a step-like manner. Here, the reference symbol OR in Fig. 14A of the drawing indicates the direction of orientation control performance applied to the 15 orientation film (the so-called rubbing direction). Further, an area indicated virtually between dashed lines represents a light blocking film (a black matrix) BM, which is usually formed on the second substrate. Here, the indication of the light blocking film BM is used in respective drawings to be referred to hereinafter in the same manner.

With respect to a liquid crystal display device having such a constitution, in 20 the periphery of the unit pixel, particularly between the common signal line CL and the pixel electrode PX, or the common electrode in the vicinity of the thin film transistor TFT, an undesired electric field is generated, and this electric field gives rise to a drawback in that liquid crystal molecules of the liquid crystal layer are switched without regard to the image data, thus generating a so-called image 25 retention, whereby the image quality is degraded.

Figs. 15A to 15C are diagrammatic views showing part of the structure in the vicinity of the thin film transistor TFT in Fig. 14A, wherein Fig. 15A is a plan view, Fig. 15B is a cross sectional view taken along a line B-B' in Fig. 15A, and Fig. 15C is a cross sectional view taken along a line C-C' in Fig. 15A.

5 Reference symbol ZN in Fig. 15A indicates a region where the image retention is liable to occur, reference symbol E in Figs. 15B and 15C indicates an electric field which becomes a cause of the occurrence of image retention, and reference symbol Ef in Fig. 15C indicates a particularly strong electric field. That is, as shown in Fig. 15B and Fig. 15C, due to the electric field E that is generated  
10 between the common signal line CL and the pixel electrode PX, as well as due to the electric field E generated between the source electrode SD and the common electrode CT, the liquid crystal molecules of the liquid crystal layer are switched on and off in an undesirable manner. Further, the strong electric field Ef, having a large component in the direction which intersects the liquid crystal molecules, is generated  
15 between an edge of the source electrode SD and the common signal line CL, which are disposed close to each other, and, hence, a large image retention is generated. As a result, the inventors of the present invention have found that, due to switching of the liquid crystal molecules without regard to a normal switching operation of the unit pixel, irregularities are generated in the light (or reflection light), whereby the  
20 image quality may be degraded.

Accordingly, it is an object of the present invention to provide a liquid crystal display device in which the problem of image retention is further suppressed compared to the above-mentioned liquid crystal display device having the structure shown in Fig. 14, whereby an image display having a high image quality can be  
25 obtained.

To achieve such an object, the present invention adopts respective electrode structures in which an electric field between a common signal line and a pixel electrode is blocked, an electric field between a source electrode of a thin film transistor and a common electrode is blocked or an electric field between an edge of the source electrode and the common signal line is blocked.

The details of these electrode structures will become more apparent from a description of respective embodiments which represent various examples of technical concepts of the invention, in conjunction with the drawings.

## 10 BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1A is a plan view and Fig. 1B is a sectional view taken along line A-A' in Fig. 1A, showing a unit pixel representing a first embodiment of a liquid crystal display device according to the present invention;

Fig. 2A is a plan view and Figs. 2B and 2C are sectional views taken along lines B-B' and C-C', respectively, in Fig. 2A, showing the detailed structure in the vicinity of the thin film transistor TFT in Fig. 1A;

Fig. 3A is a plan view and Fig. 3B is a sectional view taken along line A-A', showing the vicinity of a unit pixel in a second embodiment of a liquid crystal display device according to the present invention;

Fig. 4A is a plan view and Figs. 4B and 4C are sectional views taken along lines B-B' and C-C', respectively, in Fig. 4A, showing the detailed structure in the vicinity of the thin film transistor TFT in Fig. 3A;

Fig. 5A is a plan view and Fig. 5B is a sectional view taken along line A-A' in Fig. 5A, showing the vicinity of a unit pixel in a third embodiment of a liquid crystal display device according to the present invention;

- Fig. 6 is a plan view showing the detailed structure in the vicinity of the thin film transistor TFT in Fig. 5A;
- Fig. 7A is a plan view and Fig. 7B is a sectional view taken along line A-A' in Fig. 7A, showing the vicinity of a unit pixel in a fourth embodiment of a liquid crystal display device according to the present invention;
- 5 Fig. 8 is a plan view showing the detailed structure in the vicinity of the thin film transistor TFT in Fig. 7A;
- Fig. 9A is a plan view and Fig. 9B is a sectional view taken along line A-A' in Fig. 9A, showing the vicinity of a unit pixel in a fifth embodiment of a liquid crystal display device according to the present invention;
- 10 Fig. 10A is a plan view and Fig. 10B is a sectional view taken along line B-B' in Fig. 10A, showing the detailed structure in the vicinity of the thin film transistor TFT in Fig. 9A;
- Fig. 11A is a plan view and Fig. 11B is a sectional view taken along line A-A' in Fig. 11A, showing the vicinity of a unit pixel in a sixth embodiment of a liquid crystal display device according to the present invention;
- 15 Fig. 12 is a plan view showing the detailed structure in the vicinity of the thin film transistor TFT in Fig. 11A;
- Fig. 13 is a diagram showing an example of a unit pixel portion and a peripheral portion of the liquid crystal display device of the present invention;
- 20 Fig. 14A is a plan view and Fig. 14B is a sectional view taken along line A-A' in Fig. 14A, showing the vicinity of one pixel of a liquid crystal display device which employs the IPS method; and

Fig. 15A is a plan view and Figs. 15B and 15C are sectional views taken alone lines B-B' and C-C', respectively, in Fig. 15A, showing the detailed structure in the vicinity of the thin film transistor TFT in Fig. 14A.

## 5 DETAILED DESCRIPTION OF THE INVENTION

Hereinafter, several embodiments of the present invention will be explained in detail in conjunction with the accompanying drawings.

Fig. 1A shows the constitution of a unit pixel in a first embodiment of a liquid crystal display device according to the present invention, and Fig. 1B is a cross sectional view taken along a line A-A' in Fig. 1A. Further, Fig. 2A shows the detailed structure in the vicinity of the thin film transistor TFT in Fig. 1A, Fig. 2B is a cross sectional view taken along a line B-B' in Fig. 2A and Fig. 2C is a cross sectional view taken along a line C-C' in Fig. 2A. In Fig. 1A and Fig. 2A, the same reference symbols are used to identify elements correspondingly identified in the above-mentioned Fig. 14A and Fig. 15A, and, hence, a repeated explanation of such elements will be omitted. The same goes for the respective embodiments to be described hereinafter.

As seen in Fig. 1A and Fig. 2A, on an inner surface of a unit pixel which is formed on a first substrate SUB1, a plurality of scanning signal lines GL extend in the X direction and are arranged in parallel in the Y direction, which intersects the X direction; a plurality of data signal lines DL extend in the Y direction and are arranged in parallel in the X direction; common signal lines CL are disposed close to the scanning signal lines GL and extend in the X direction, while being arranged in parallel in the Y direction; a plurality of thin film transistors TFT are arranged at intersecting portions of the scanning signal lines GL and the data signal lines DL;

pixel electrodes PX are provided which are driven by the thin film transistors TFT; and common electrodes CT are connected to the common signal line CL and are alternately arranged with respect to the pixel electrodes PX, such that the common electrode CT is arranged close to the pixel electrode PX in the X direction. Thus, the 5 unit pixel is formed in a region surrounded by the scanning signal lines GL and the data signal lines DL.

The common electrodes CT are arranged in a superposed manner over the common signal line CL by way of an insulation layer, which is constituted of an inorganic insulation layer PAS and an organic insulation layer OPAS that is stacked 10 on the inorganic insulation layer PAS. The same goes for all of the respective embodiments. However, in place of the organic insulation layer OPAS, an inorganic insulation layer may be used. Also in this case, the same goes for the respective embodiments to be described hereinafter. The pixel electrode PX is connected to the source electrode SD of the thin film transistor TFT via the through hole SH, which 15 penetrates the insulation layer that is formed of the above-mentioned inorganic insulation layer PAS and organic insulation layer OPAS. The common electrodes CT are formed such that they extend (project) into the inside of the unit pixel so as to cover the common signal line CL, whereby an electric field E between the common signal line CL and the pixel electrode PX is blocked.

20 The pixel electrode PX has an end portion extending toward the inside of the unit pixel from the common electrode CT, while the source electrode SD has a first projecting portion SD1 and a second projecting portion SD2 which project in a step-like manner in a direction which intersects with the extending direction of the common electrodes CT. The step-like first projecting portion SD1 (source electrode 25 side) is arranged between the common signal line CL and the pixel electrode PX

and, at the same time, at a position which is sealed or blocked by the common electrode CT. Further, the step-like second projecting portion SD2 (a portion which further projects toward the unit pixel side from the first projecting portion SD1) is connected to the pixel electrode PX via the through hole SH at a position where the 5 second projecting portion SD2 is superposed on the pixel electrode PX.

Further, assuming that the distance between an edge of the second projecting portion parallel to the Y direction and the pixel electrode PX neighboring in the X direction is "a", the distance between the pixel electrode PX and an end portion of the first projecting portion in the Y direction is "b", and the distance between the 10 pixel electrode PX and the common electrode CT neighboring in the Y direction is "c", the relationships  $a > c$  and  $b > c$  are established. Further, the relationships  $a > c$  and  $b > c$  may be established. Due to the setting of such relationships, the electric field generated between the common signal line and the pixel electrode is blocked, or the electric field E generated between the source electrode SD of the thin film transistor 15 TFT and the common electrode CT is blocked. Further, the electric field E generated between the edge of the source electrode SD and the common signal line CL is also blocked.

According to this embodiment, switching of the liquid crystal molecules without regard to the normal switching operation of the unit pixel is suppressed, and, 20 hence, no irregularities are generated with respect to the transmitting light (or a reflection light) of the liquid crystal layer, whereby a high-quality image display can be obtained.

Fig. 3A shows the vicinity of a unit pixel in the second embodiment of a liquid crystal display device according to the present invention, and Fig. 3B is a cross 25 sectional view taken along a line A-A' in Fig. 3A. Further, Fig. 4A shows the vicinity

of the thin film transistor TFT in Fig. 3A, while Fig. 4B is a cross sectional view taken along a line B-B' in Fig. 4A and Fig. 4C is a cross sectional view taken along a line C-C' in Fig. 4A.

As seen in Fig. 3A and Fig. 4A, on an inner surface of the unit pixel formed

5 on a first substrate SUB1, a plurality of scanning signal lines GL extend in the X direction and are arranged in parallel in the Y direction, which intersects the X direction; a plurality of data signal lines DL extend in the Y direction and are arranged in parallel in the X direction; common signal lines CL are disposed close to the scanning signal lines GL and extend in the X direction, while being arranged in

10 parallel in the Y direction; a plurality of thin film transistors TFT are arranged at intersecting portions of the scanning signal lines GL and the data signal lines DL; pixel electrodes PX are provided which are driven by the thin film transistors TFT; and common electrodes CT are connected to the common signal line CL and are arranged with respect to the pixel electrodes PX such that the common electrode CT

15 is arranged close to the pixel electrode PX in the X direction. Thus, the unit pixel is formed in a region surrounded by the scanning signal lines GL and the data signal lines DL.

The pixel electrode PX is arranged in a superposed manner over the common signal line CL by way of an insulation layer (an inorganic insulation layer

20 PAS and an organic insulation layer OPAS) and is connected to a source electrode SD of the thin film transistor TFT via a through hole SH, which penetrates the above-mentioned insulation layer. Further, a portion of the pixel electrode PX overhangs over the common signal line CL beyond the unit pixel, and an overhanging distal end forms an extending portion PXJ, which is spaced from a side of the common signal

25 line CL at a side opposite to the pixel electrode. The common electrodes CT are

formed such that they extend in the inside of the unit pixel covering the common signal line CL, except for a portion along the extending portion PXJ of the pixel electrode PX. Due to such a constitution, an electric field E between the common signal line CL and the pixel electrode PX can be blocked.

5           Further, the extending portion PXJ of the pixel electrode PX has a width that is larger than the width of the other portion of the pixel electrode PX in the X direction, and the source electrode SD has a projecting portion SD3 which projects in a step-like manner in the direction which intersects the extending direction of the common signal line CL. The step-like projecting portion SD3 is arranged between  
10          the common signal line CL and the common electrode CT and is disposed at a position which is blocked by the common electrode CT. The step-like projecting portion SD3 is arranged above the common signal line CL and at a position where the projecting portion SD3 is superposed on an extending portion PXJ of the pixel electrode PX and is connected to the pixel electrode PX via the through hole SH.

15          Then, assuming that the distance of the step-like projecting portion SD3 of the source electrode SD from an end portion of the common signal line CL in the Y direction is "a", the distance in the X direction of the edge in the Y direction parallel to the common signal line CL contiguous with the extending portion PXJ of the pixel electrode PX is "b", and the distance in the X direction between the edge in the Y direction of the overhanging portion PXJ of the pixel electrode PX and the common electrode CT is "c", the relationship  $a \geq 0$  is established. Further, the relationship  $b > c \times 2.0$  is established. Further, the relationship  $a \geq 0$  and  $b > c \times 2.0$  is established.  
20          Due to the setting of such relationships, the electric field E between the common signal line CL and the pixel electrode PX is blocked, or the electric field E generated  
25          between the source electrode SD of the thin film transistor TFT and the common

electrode CT is blocked. Further, the electric field E generated between the edge of the source electrode SD and the common signal line CL is also blocked.

According to this embodiment, a switching of the liquid crystal molecules without regard to the normal switching operation of the unit pixel is suppressed, and, 5 hence, no irregularities are generated with respect to the transmitting light (or reflection light) of the liquid crystal layer, whereby a high-quality image display can be obtained.

Fig. 5A shows the vicinity of a unit pixel in a third embodiment of a liquid crystal display device according to the present invention, and Fig. 5B is a cross 10 sectional view taken along a line A-A' in Fig. 5A. Further, Fig. 6A shows the detailed structure in the vicinity of the thin film transistor TFT in Fig. 5A.

As seen in Fig. 5A and Fig. 6A, on an inner surface of the unit pixel formed on a first substrate SUB1, a plurality of scanning signal lines GL extend in the X direction and are arranged in parallel in the Y direction; a plurality of data signal lines 15 DL extend in the Y direction and are arranged in parallel in the X direction; common signal lines CL are disposed close to the scanning signal lines GL and extend in the X direction, while being arranged in parallel in the Y direction; a plurality of thin film transistors TFT are arranged at intersecting portions of the scanning signal lines GL and the data signal lines DL; pixel electrodes PX are provided, which are driven by 20 the thin film transistors TFT; and common electrodes CT are connected to the common signal line CL and are arranged with respect to the pixel electrodes PX such that the common electrode CT is arranged close to the pixel electrode PX in the X direction. Thus, the unit pixel is formed in a region surrounded by the scanning signal lines GL and the data signal lines DL.

The pixel electrode PX is arranged in a superposed manner over the common signal line CL by way of an insulation layer (an inorganic insulation layer PAS and an organic insulation layer OPAS) and is connected to a source electrode SD of the thin film transistor TFT via a through hole SH, which penetrates the

5 insulation layer(the inorganic insulation layer PAS and the organic insulation layer OPAS). Further, a portion of the pixel electrode PX includes an enlarged portion PXE which bridges over the common signal line CL from the inside of the unit pixel.

The common electrodes CT are formed such that they extend in the inside of the unit pixel covering the common signal line CL, except for a portion along the enlarged

10 portion PXE of the pixel electrode PX. Due to such a constitution, an electric field between the common signal line CL and the pixel electrode PX can be blocked.

The enlarged portion PXE of the pixel electrode PX has an approximately rectangular shape, having two sides along the X direction of the unit pixel and another two sides along the Y direction, wherein the unit pixel side of the two sides

15 along the X direction extends into and is positioned in the inside of the unit pixel beyond the common signal line CL, while the side opposite to the unit pixel of the two sides along the X direction is positioned inside an edge of the common signal line CL at a side opposite to the unit pixel and outside an edge of the source electrode SD at a side opposite to the unit pixel.

20 Then, assuming the distance between the pixel electrode PX which extends in the Y direction from the enlarged portion PXE to the inside of the unit pixel and the common electrode CT which is arranged close to the pixel electrode PX in the X direction is “a”, the distance between two sides of the enlarged portion PXE along the X direction and the common electrodes CT which are arranged close to the

25 enlarged portion PXE is “b” and the distance between the unit pixel-PX-side out of

the two sides along the X direction and the common signal line CL is "c", a  
relationship  $a>b$  is established or a relationship  $b\times 0.5 < c$  is established. Further,  
relationship relationships  $a>b$  and  $b\times 0.5 < c$  are established. In this manner, by  
enlarging the pixel electrode PX, a region which can block the common signal line  
5 CL can be enlarged, whereby the electric field E between the common signal line CL  
and the pixel electrode PX can be blocked, or the electric field between the source  
electrode SD of the thin film transistor TFT and the common electrode CT can be  
blocked. Further, the electric field between the edge of the source electrode SD and  
the common signal line CL also can be blocked.

10 Fig. 7A shows the vicinity of a unit pixel in a fourth embodiment of a liquid  
crystal display device according to the present invention, and Fig. 7B is a cross  
sectional view taken along a line A-A' in Fig. 7A. Further, Fig. 8A shows the detailed  
structure in the vicinity of the thin film transistor TFT in Fig. 7A.

This embodiment is characterized in that portions of the common signal line  
15 CL have the width thereof narrowed at both sides of the projecting portion SD4 of the  
source electrode SD as compared to the embodiment 3, wherein a unit-pixel-side  
edge of the common signal line CL is retracted such that the distance between the  
unit-pixel-side edge of the common signal line CL and the unit-pixel-side side in the  
X direction of the enlarged portion PXE of the pixel electrode PX is increased. That  
20 is, in the region of a portion of the source electrode SD2 which is covered with the  
enlarged portion PXE of the pixel electrode PX, in the X direction of the projecting  
portion SD4, which projects toward the unit pixel side and also projects in a step-like  
manner at the unit pixel side beyond the common signal line CL, a unit-pixel-side  
edge of the common signal line CL, which is arranged at the data signal line DL side

is retracted at a side opposite to the unit pixel and the relationship of the distance a > distance b is established.

According to this embodiment, in addition to the advantageous effects of the third embodiment, a switching of the liquid crystal molecules without regard to the 5 normal switching operation of the unit pixel can be further suppressed, and, hence, no irregularities are generated with respect to the transmitting light (or reflection light) of the liquid crystal layer, whereby a high-quality image display can be obtained.

Fig. 9A shows the vicinity of a unit pixel in a fifth embodiment of a liquid crystal display device according to the present invention, and Fig. 9B is a cross 10 sectional view taken along a line A-A' in Fig. 9A. Further, Fig. 10A shows the detailed structure in the vicinity of the thin film transistor TFT in Fig. 9A, while Fig. 10A is a plan view and Fig. 10B is a cross-sectional view taken along a line B-B' in Fig. 10A.

As seen in Fig. 9A and Fig. 10A, on an inner surface of the unit pixel formed 15 on a first substrate SUB1, a plurality of scanning signal lines GL extend in the X direction and are arranged in parallel in the Y direction; a plurality of data signal lines DL extend in the Y direction and are arranged in parallel in the X direction; common signal lines CL are disposed close to the scanning signal lines GL and extend in the X direction, while being arranged in parallel in the Y direction; a plurality of thin film 20 transistors TFT are arranged at intersecting portions of the scanning signal lines GL and the data signal lines DL; pixel electrodes PX are provided, which are driven by the thin film transistors TFT; and common electrodes CT are connected to the common signal line CL and are arranged with respect to the pixel electrodes PX such that the common electrode CT is arranged close to the pixel electrode PX in the

X direction. Thus, the unit pixel is formed in a region surrounded by the scanning signal lines GL and the data signal lines DL.

The pixel electrode PX is arranged in a superposed manner over the common signal line CL by way of an insulation layer (an inorganic insulation layer

5 PAS and an organic insulation layer OPAS) and is connected to a source electrode SD of the thin film transistor TFT via a through hole SH, which penetrates the insulation layer (the inorganic insulation layer PAS and the organic insulation layer OPAS). Further, a portion of the pixel electrode PX includes an enlarged portion PXE bridges strides over the common signal line CL from the inside of the unit pixel.

10 The common electrodes CT are formed such that they extend in the inside of the unit pixel covering the common signal line CL, except for a portion along the enlarged portion PXE of the pixel electrode PX, and, hence, an electric field between the common signal line CL and the pixel electrode PX can be blocked. The enlarged portion PXE of the pixel electrode PX has two sides along the X direction of the unit pixel and two sides along the Y direction. The unit pixel side of the two sides along the X direction is positioned inside a unit-pixel-side edge (side) of the common signal line CL, while the side of two sides opposite to the unit pixel PX is positioned outside the edge of the common signal line CL opposite to the unit pixel and outside the edge of the enlarged portion PXE of the pixel electrode PX opposite to the unit pixel.

15 A side of the source electrode SD of the thin film transistor TFT opposite to the unit pixel extends in the X direction close to the thin film transistor TFT to form one side of an extending portion SDE, while another side of the source electrode SD at the unit pixel side extends in the X direction parallel to one side of the extending portion SDE at the side opposite to the unit pixel from another of the two sides in the

20 Y direction close to the thin film transistor TFT. Then, assuming that the distance

between another side of the extending portion SDE and the edge of the common signal line CL opposite to the unit pixel PX is "a" and the distance between the side opposite to the unit pixel PX of another two sides of the source electrode SD and the neighboring common electrode in the X direction is "b", these distances are set as  
5       $a \geq 0$  and  $b \geq 0$ .

Accordingly, as shown in Fig. 10B, a portion where a fringe electric field Ef, which constitutes a strong electric field formed between the source electrode SD and the common signal line CL, can be completely blocked by the pixel electrode PX, and, hence, switching of liquid crystal molecules without regard to the normal  
10     switching operation of the unit pixel can be further suppressed, whereby no irregularities are generated with respect to the transmitting light (or reflection light) of the liquid crystal layer, whereby a high-quality image display can be obtained.

Fig. 11A shows the vicinity of a unit pixel in a sixth embodiment of a liquid crystal display device according to the present invention, and Fig. 11B is a cross  
15     sectional view taken along a line A-A' in Fig. 11A. Further, Fig. 12A shows the detailed structure in the vicinity of the thin film transistor TFT in Fig. 11A.

As seen in Fig. 11A and Fig. 12A, on an inner surface of the unit pixel formed on a first substrate SUB1, a plurality of scanning signal lines GL extend in the X direction and are arranged in parallel in the Y direction; a plurality of data signal  
20     lines DL extend in the Y direction and are arranged in parallel in the X direction; common signal lines CL are disposed close to the scanning signal lines GL and extend in the X direction, while being arranged in parallel in the Y direction, a plurality of thin film transistors TFT are arranged at intersecting portions of the scanning signal lines GL and the data signal lines DL; pixel electrodes PX are  
25     provided, which are driven by the thin film transistors TFT; and common electrodes

CT are connected to the common signal line CL and are arranged with respect to the pixel electrodes PX such that the common electrode CT is arranged close to the pixel electrode PX in the X direction. Thus, the unit pixel is formed in a region surrounded by the scanning signal lines GL and the data signal lines DL.

5       The pixel electrode PX is arranged in a superposed manner over the common signal line CL by way of an insulation layer (an inorganic insulation layer PAS and an organic insulation layer OPAS) and is connected to a source electrode SD of the thin film transistor TFT via a through hole SH, which penetrates the insulation layer (the inorganic insulation layer PAS and the organic insulation layer

10      OPAS).

Further, a portion of the pixel electrode PX includes an approximately rectangular enlarged portion PXE which bridges over the common signal line CL from the inside of the unit pixel, while the source electrode SD includes a projecting portion SD4 which projects toward the unit pixel side at a portion which is covered  
15      with the enlarged portion PXE of the pixel electrode PX and projects toward the unit pixel side in a step-like manner beyond the common signal line.

The common electrode CT is formed such that it extends (overhangs) in the inside of the unit pixel, while covering the common signal line CL, except for a portion of the common signal line CL, along the enlarged portion PXE of the pixel  
20      electrode PX so as to block an electric field between the common signal line CL and the pixel electrode PX. At the same time, the common electrode CT includes common electrode enlarged portions CTE, which are enlarged to cover the common signal lines CL corresponding to the enlarged portion PXE of the pixel electrode PX at portions of the thin film transistor TFT side, while having sides which extend at an  
25      angle  $\theta$  in the X direction with respect to the Y direction and sides which extend in

the Y direction. Here, an angle at which the pixel electrode PX in the unit pixel rises is also set to  $\theta$ .

The enlarged portions PXE of the pixel electrode PX include two sides along the X direction of the unit pixel and two other sides which extend in the Y direction of  
5 the unit pixel, wherein the unit pixel side of the two sides along the X direction is positioned more inside of the unit pixel than the common signal line CL and the other side of the two sides opposite to the unit pixel is positioned more inside than an edge the common signal line CL opposite to the unit pixel.

Here, by setting the angle  $\theta$  to  $90^\circ \leq \theta < 180^\circ$ , the pixel electrode PX and the  
10 common electrode CT are enlarged. Further, the relationship between the distance “a” between the side of the common electrode enlarged portion CTE which extends in the X direction and the step-like edge extending along the first direction, the distance “b” between the unit-pixel-side edge of the common signal line CL and the edge along the X direction of the projecting portion SD4 of the source electrode SD  
15 which projects in a step-like manner, and the distance “c” between the pixel electrode PX and the edge in the Y direction of the common electrode enlarged portion CTE is set to  $a > b$ , and is preferably set to  $(a-b) > c$ . Due to such a constitution, an electric field from the common signal line CL can be surely blocked, and, hence, the image retention areas are reduced, whereby the effective numerical  
20 aperture can be increased.

Here, in the above-mentioned respective embodiments, by setting the distance between the pixel electrode PX and the source electrode SD in Fig. 1A to be smaller than the distance between the pixel electrode PX and the common electrode CT, which are arranged to close each other, for example, a DC component  
25 which remains between the electrodes can be reduced; and, hence, the image

retention can be suppressed, and, at the same time, the effective numerical aperture is enhanced. Here, as shown in Fig. 14A, by forming a black matrix BM such that the black matrix BM covers the longitudinal end portions of the pixel electrodes PX and the common electrodes CT inside of the unit pixel, a degradation of image quality attributed to image retention can be further suppressed.

Further, in the above-mentioned respective embodiments, by further arranging the common signal lines CL along both sides of the scanning signal line GL, it is possible to more effectively block the leaking of the electric field from the scanning signal line GL. Here, the end portions of the common electrodes CT are formed such that the end portions are retracted from the common electrodes CT inside of the unit pixel.

Fig. 13 is a schematic cross-sectional view showing one example of the unit pixel portion and the peripheral portion of the liquid crystal display device of the present invention. In the drawing, SUB1 indicates the first substrate which has been identified in conjunction with the respective embodiments. A first orientation film ORI1 is applied to an uppermost layer on the first substrate SUB1, which is brought into contact with the liquid crystal layer LC, and rubbing treatment is applied to the first orientation film. Then, a first polarizer POL1 is formed on an outer surface of the substrate SUB2. Here, the same reference symbols are used to identify identical functional portions in the respective embodiments, wherein PSV indicates a protective film and the constitution of the first substrate SUB1 is simplified.

Further, SUB2 indicates the second substrate, which constitutes the counter substrate, wherein the second substrate SUB2 includes color filters CF, which are defined by the black matrix BM, and an overcoat layer OC, which is formed above the color filters CF. Further, a second orientation film ORI2, which is brought into

contact with the liquid crystal layer LC, is applied to the overcoat layer OC, and rubbing treatment is applied to the second orientation film ORI2. A second polarizer POL2 is mounted on an outer surface (a viewing side) of the second substrate SUB2. A sealing material SL seals between peripheral portions of the first substrate

5 SUB1 and the second substrate SUB2.

The electric field EP which effects switching of the unit pixel (turning on/off of the unit pixel) is formed between the pixel electrode PX and the common electrode CT in the direction parallel to the respective substrate surfaces.

As has been explained heretofore, according to the present invention, the  
10 generation of an undesired electric field between the common signal lines and the pixel electrodes and the common electrodes in the periphery of the unit pixel, particularly in the vicinity of the thin film transistor, can be suppressed, and, hence, the occurrence of image retention can be reduced, whereby it is possible to provide a liquid crystal display device which is capable of displaying high quality images.

**VERSION WITH MARKINGS TO SHOW CHANGES MADE****LIQUID CRYSTAL DISPLAY DEVICE****BACKGROUND OF THE INVENTION****1. Field of the Invention**

The present invention relates to a liquid crystal display device in which a technique is employed for suppressing the generation of undesired electric fields that can be the cause of image retention, which tends to degrade the image quality of the display device.

**2. Description of the Related Art**

In a liquid crystal display device, a liquid crystal layer formed of liquid crystal molecules is sandwiched between two sheets of insulation insulating substrates, preferably made of glass, and, at the same time, at least a pair of electrodes for applying an electric field to the liquid crystal layer are provided to either one or both of the insulation-substrates. In an IPS (in-plane switching) type of liquid crystal display device, all of the above-mentioned electrodes for applying the an electric field to the liquid crystal layer are formed on one substrate, and the pixels are turned on and off (that is, switching is effected) by forming so as to selectively form an electric field having components that are parallel to athe substrate surface with respect to the liquid crystal layer in selected pixels.

**SUMMARY OF THE INVENTION**

Fig. 14 is an explanatory view Figs. 14A and 14B are diagrammatic views of the vicinity of one pixel of a liquid crystal display device adopting in which an IPS method is employed, wherein Fig. 14A is a plan view and Fig. 14B is a cross

sectional view take along a line A-A' in Fig. 14A. To an inner surface of one substrate (first substrate, usually referred to as the thin film transistor substrate, since it constitutes a substrate on which thin film transistors are formed, as will be explained hereinafter) SUB1 of a pair of substrates which constitute the liquid crystal display device, the following pixel structure is provided. Then, a second substrate SUB2 (not shown in the drawing) on which color filters and the like are formed is laminated to the first substrate SUB1, and a liquid crystal layer is sandwiched and sealed in a gap defined between both substrates.

On an inner surface of one pixel (also referred to as a unit pixel hereinafter), a plurality of scanning signal lines GL which extend in the first direction (referred to as X direction hereinafter) and are arranged in parallel in the second direction (referred to as Y direction hereinafter) which intersects the X direction; a plurality of date signal lines DL which extend in the Y direction and are arranged in parallel in the X direction; common signal lines (also referred to as common storage lines) CL which are disposed close to the scanning signal lines GL, and extend in the X direction, and are while being arranged in parallel in the Y direction; a plurality of thin film transistors TFT which are arranged at intersecting portions of the scanning signal lines GL and the date signal lines DL; pixel electrodes PX which are driven by the thin film transistors TFT; and common electrodes CT which are connected to the common signal line CL and are alternately arranged with respect to the pixel electrodes PX such that the common electrode CT is arranged close to the pixel electrode PX in the X direction are formed. ThenThus, unit pixels are formed in regions which are surrounded by the scanning signal lines GL and the data signal lines DL.

As shown in Fig. 14B, ~~with respect to which shows~~ a cross section taken along a line A-A' in Fig. 14A, the common signal line CL is formed on the first substrate SUB1, and a ~~date~~data signal line DL and a source electrode SD of the thin film transistor TFT are formed on the common signal line CL by way of a gate insulation layer GI. These electrodes or wiring are covered with a laminated film which is constituted of an inorganic insulation layer PAS and an organic insulation layer OPAS, and the pixel electrodes PX and the common electrodes CT are formed on the laminated film. Although an orientation film which is brought into contact with a liquid crystal layer (not shown in the drawing) is formed over the pixel electrodes PX and the common electrodes CT, these elements are omitted from the drawing here.

The pixel electrodes PX and the common electrodes CT are alternately arranged close to each other in a comb-teeth shape. As shown in Fig. 14B, the pixel electrodes PX are connected to the source electrode SD, which constitutes an output electrode of the thin film transistor TFT, via a through hole SH. As shown in Fig. 14A, the source electrode SD is superposed on the common signal line CL, and a portion which forms the through hole SH projects into the inside of the unit pixel region in a step-like manner. Here, ~~the~~ reference symbol OR in Fig. 14A of the drawing indicates the direction of orientation control performance applied to the orientation film (~~the~~ so-called rubbing direction). Further, ~~a range an area indicated by a bold dotted line virtually indicates between dashed lines represents~~ a light blocking film (a black matrix) BM, which is usually formed on the second substrate. Here, the indication of the light blocking film BM is used in respective drawings ~~explained to be referred to~~ hereinafter in the same manner.

With respect to ~~the-a~~ liquid crystal display device having such a constitution, in ~~a~~~~the~~ periphery of the unit pixel, particularly between the common signal line CL and the pixel electrode PX, or the common electrode in the vicinity of the thin film transistor TFT, an undesired electric field is generated, and this electric field gives rise to a drawback ~~in~~ that liquid crystal molecules of the liquid crystal layer are switched ~~irrelevant~~~~without regard to~~ ~~the~~ image data, thus generating a so-called image retention, whereby ~~the~~ image quality is degraded.

~~Fig. 15 is an explanatory view of an essential Figs. 15A to 15C are diagrammatic views showing part of the detail structure of~~ ~~in~~ the vicinity of the thin film transistor TFT in Fig. 14A, wherein Fig. 15A is a plan view, Fig. 15B is a cross sectional view taken along a line B-B' in Fig. 15A, and Fig. 15C is a cross sectional view taken along a line C-C' in Fig. 15A.

Reference symbol ZN in Fig. 15-15A indicates a region where the image retention is liable to occur, reference symbol E in Figs. 15B and 15C indicates an electric field which becomes a cause of the occurrence of image retention, and reference symbol Ef in Fig. 15C indicates a particularly strong electric field. That is, as shown in Fig. 15B and Fig. 15C, due to the electric field E ~~that is~~ generated between the common signal line CL and the pixel electrode PX, as well as due to the electric field E generated between the source electrode SD and the common electrode CT, the liquid crystal molecules of the liquid crystal layer are switched on and off in an undesirable manner. Further, the strong electric field Ef, having a large component in the direction which intersects the liquid crystal molecules, is generated between an edge of the source electrode SD and the common signal line CL, which are disposed close to each other, and, hence, a large image retention is generated. As a result, ~~the~~ inventors of the present invention have found that, due to switching

of the liquid crystal molecules ~~irrelevant without regard~~ to a normal switching operation of the unit pixel, ~~irregularities irregularities~~ are generated in a transmitting ~~the~~ light (or a reflection light), whereby the image quality may be degraded.

Accordingly, it is an ~~advantage object~~ of the present invention that ~~the to~~ provide a liquid crystal display device in which the problem of image retention is further suppressed compared to the above-mentioned liquid crystal display device having the structure shown as ~~in~~ Fig. 14, whereby ~~an~~ image display having ~~a~~ high image quality can be obtained.

To achieve such an ~~advantage object~~, the present invention adopts respective electrode structures in which an electric field between a common signal line and a pixel electrode is blocked, an electric field between a source electrode of a thin film transistor and a common electrode is blocked or an electric field between an edge of the source electrode and the common signal line is blocked.

The ~~detail details~~ of these electrode structures will become ~~more~~ apparent in ~~view from a description~~ of respective embodiments which ~~express represent~~ various examples ~~as of~~ technical concepts ~~of the invention~~, in conjunction with ~~the~~ drawings.

## BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1A is an explanatory ~~a plan view of the constitution of the vicinity of~~ and Fig. 1B is a sectional view taken along line A-A' in Fig. 1A, showing a unit pixel ~~representing a~~ first embodiment of a liquid crystal display device according to the present invention;

Fig. 2 is an explanatory ~~view of an essential part of~~ Fig. 2A is a plan view and Figs. 2B and 2C are sectional views taken along lines B-B' and C-C',

respectively, in Fig. 2A, showing the detailed structure of in the vicinity of a the thin film transistor TFT in Fig. 1A;

Fig. 3 is an explanatory view of the constitution of Fig. 3A is a plan view and Fig. 3B is a sectional view taken along line A-A', showing the vicinity of a unit pixel in the a second embodiment of a liquid crystal display device according to the present invention;

Fig. 4 is an explanatory view of an essential part of Fig. 4A is a plan view and Figs. 4B and 4C are sectional views taken along lines B-B' and C-C', respectively, in Fig. 4A, showing the detailed structure of in the vicinity of a the thin film transistor TFT in Fig. 3A;

Fig. 5 is an explanatory view of the constitution of Fig. 5A is a plan view and Fig. 5B is a sectional view taken along line A-A' in Fig. 5A, showing the vicinity of a unit pixel in the a third embodiment of a liquid crystal display device according to the present invention;

Fig. 6 is an explanatory view of an essential part of a plan view showing the detailed structure of in the vicinity of a the thin film transistor TFT in Fig. 5A;

Fig. 7 is an explanatory view of the constitution of Fig. 7A is a plan view and Fig. 7B is a sectional view taken along line A-A' in Fig. 7A, showing the vicinity of a unit pixel in the a fourth embodiment of a liquid crystal display device according to the present invention;

Fig. 8 is an explanatory view of an essential part of a plan view showing the detailed structure of in the vicinity of a the thin film transistor TFT in Fig. 7A;

Fig. 9 is an explanatory view of the constitution of Fig. 9A is a plan view and Fig. 9B is a sectional view taken along line A-A' in Fig. 9A, showing the vicinity of a

unit pixel in the a fifth embodiment of a liquid crystal display device according to the present invention;

~~Fig. 10 is an explanatory view of an essential part of Fig. 10A is a plan view and Fig. 10B is a sectional view taken along line B-B' in Fig. 10A, showing the detailed structure of in the vicinity of a the thin film transistor TFT in Fig. 9 9A;~~

~~Fig. 11 is an explanatory view of the constitution of Fig. 11A is a plan view and Fig. 11B is a sectional view taken along line A-A' in Fig. 11A, showing the vicinity of a unit pixel in the a sixth embodiment of a liquid crystal display device according to the present invention;~~

~~Fig. 12 is an explanatory view of an essential part of a plan view showing the detailed structure of in the vicinity of a the thin film transistor TFT in Fig. 11 11A;~~

~~Fig. 13 is a schematic cross-sectional view for explaining a constitutional diagram showing an example of a unit pixel portion and a peripheral portion of the liquid crystal display device of the present invention;~~

~~Fig. 14 is an explanatory view of Fig. 14A is a plan view and Fig. 14B is a sectional view taken along line A-A' in Fig. 14A, showing the vicinity of one pixel of a liquid crystal display device adopting an which employs the IPS method; and~~

~~Fig. 15 is an explanatory view of an essential part of Fig. 15A is a plan view and Figs. 15B and 15C are sectional views taken alone lines B-B' and C-C', respectively, in Fig. 15A, showing the detailed structure of in the vicinity of a the thin film transistor TFT in Fig. 14 14A.~~

## DETAILED DESCRIPTION OF THE INVENTION

Hereinafter, several embodiments of the present invention ~~are will be~~  
explained in detail in conjunction with the accompanying drawings which show  
respective embodiments.

~~Fig. 1 is an explanatory view of 1A shows~~ the constitution of the vicinity of a unit pixel in ~~the~~a first embodiment of a liquid crystal display device according to the present invention, ~~wherein Fig. 1A is a plan view and Fig. 1B is a cross sectional view taken along a line A-A' in Fig. 1A. Further, Fig. 2 is an explanatory view of an essential part of 2A shows~~ the detailed structure in the vicinity of the thin film transistor TFT in Fig. 1A, ~~wherein Fig. 2A is a plan view, Fig. 2B is a cross sectional view taken along a line B-B' in Fig. 2A and Fig. 2C is a cross sectional view taken along a line C-C' in Fig. 2A. In Fig. 1-1A and Fig. 2-2A, the same reference symbols which are equal to the reference symbols are used to identify elements correspondingly identified~~ in the above-mentioned Fig. 14-14A and Fig. 15-15A, correspond to identical functional portions and, hence, their ~~a~~ repeated explanation is ~~of such elements will be omitted. Here, the~~ The same goes for the respective embodiments to be described hereinafter.

In As seen in Fig. 1-1A and Fig. 2-2A, on an inner surface of ~~the~~a unit pixel which is formed on a first substrate SUB1, a plurality of scanning signal lines GL which extend in the X direction and are arranged in parallel in the Y direction, which intersects the X direction; a plurality of date-data signal lines DL which extend in the Y direction and are arranged in parallel in the X direction; common signal lines CL which are disposed close to the scanning signal lines GL, and extend in the X direction, and are while being arranged in parallel in the Y direction; a plurality of thin film transistors TFT which are arranged at intersecting portions of the scanning signal lines GL and the date-data signal lines DL; pixel electrodes PX are provided

which are driven by the thin film transistors TFT<sub>1</sub> and common electrodes CT which are connected to the common signal line CL and are alternately arranged with respect to the pixel electrodes PX<sub>1</sub> such that the common electrode CT is arranged close to the pixel electrode PX in the X direction are formed. Then~~Thus~~, the unit pixel is formed in a region surrounded by the scanning signal lines GL and the data signal lines DL.

The common electrodes CT are arranged in a superposed manner over the common signal line CL by way of an insulation layer, which is constituted of an inorganic insulation layer PAS and an organic insulation layer OPAS that is stacked on the inorganic insulation layer PAS. The same goes for all of the respective embodiments. However, in place of the organic insulation layer OPAS, an inorganic insulation layer may be used. Also in this case, the same goes for the respective embodiments to be described hereinafter. The pixel electrode PX is connected to the source electrode SD of the thin film transistor TFT via the through hole SH<sub>1</sub> which penetrates the insulation layer which that is formed of the above-mentioned inorganic insulation layer PAS and organic insulation layer OPAS. The common electrodes CT are formed such that ~~the common electrodes CT~~ they extend (project) into the inside of the unit pixel so as to cover the common signal line CL, whereby an electric field E between the common signal line CL and the pixel electrode PX is blocked.

The pixel electrode PX has an end portion extending toward the inside of the unit pixel from the common electrode CT, while the source electrode SD has a first projecting portion SD1 and a second projecting portion SD2 which project in a step-like manner in the-a direction which intersects with the extending direction of the common electrodes CT. The step-like first projecting portion SD1 (source electrode

side) is arranged between the common signal line CL and the pixel electrode PX and, at the same time, at a position which is sealed or blocked by the common electrode CT. Further, the step-like second projecting portion SD2 (a portion which further projects toward the unit pixel side from the first projecting portion SD1) is connected to the pixel electrode PX via the through hole SH at a position where the second projecting portion SD2 superposes is superposed on the pixel electrode PX.

Further, assuming athat the distance between an edge of the second projecting portion parallel to the Y direction and the pixel electrode PX neighboring in the X direction as-is "a", athe distance between the pixel electrode PX and an end portion of the first projecting portion in the Y direction as-is "b", and athe distance between the pixel electrode PX and the common electrode CT neighboring in the Y direction as-is "c", the relationship-relationships a>c and b>c is-are established. Further, the relationship-relationships a>c and b>c may be established. Due to such the setting of such relationships, the electric field generated between the common signal line and the pixel electrode is blocked, or the electric field E generated between the source electrode SD of the thin film transistor TFT and the common electrode CT is blocked. Further, the electric field E generated between the edge of the source electrode SD and the common signal line CL is also blocked.

According to this embodiment, switching of the liquid crystal molecules irrelevantwithout regard to the normal switching operation of the unit pixel is suppressed, and, hence, no irregularities are generated with respect to athe transmitting light (or a reflection light) of the liquid crystal layer, whereby thea high-quality image display can be obtained.

~~Fig. 3 is an explanatory view of the constitution of Fig. 3A shows~~ the vicinity of a unit pixel in the second embodiment of a liquid crystal display device according

to the present invention, wherein Fig. 3A is a plan view and Fig. 3B is a cross sectional view taken along a line A-A' in Fig. 3A. Further, Fig. 4 is an explanatory view of an essential part of the detailed structure in 4A shows the vicinity of the thin film transistor TFT in Fig. 3A, wherein Fig. 4A is a plan view, while Fig. 4B is a cross sectional view taken along a line B-B' in Fig. 4A and Fig. 4C is a cross sectional view taken along a line C-C' in Fig. 4A.

In As seen in Fig. 3-3A and Fig. 4-4A, on an inner surface of the unit pixel formed on a first substrate SUB1, a plurality of scanning signal lines GL which extend in the X direction and are arranged in parallel in the Y direction, which intersects the X direction; a plurality of date-data signal lines DL which extend in the Y direction and are arranged in parallel in the X direction; common signal lines CL which are disposed close to the scanning signal lines GL, and extend in the X direction, and are while being arranged in parallel in the Y direction; a plurality of thin film transistors TFT which are arranged at intersecting portions of the scanning signal lines GL and the date-data signal lines DL; pixel electrodes PX are provided which are driven by the thin film transistors TFT; and common electrodes CT which are connected to the common signal line CL and are arranged with respect to the pixel electrodes PX such that the common electrode CT is arranged close to the pixel electrode PX in the X direction are formed. Then Thus, the unit pixel is formed in a region surrounded by the scanning signal lines GL and the data signal lines DL.

The pixel electrode PX is arranged in a superposed manner over the common signal line CL by way of an insulation layer (an inorganic insulation layer PAS and an organic insulation layer OPAS) and is connected to a source electrode SD of the thin film transistor TFT via a through hole SH, which penetrates the above-mentioned insulation layer. Further, a portion of the pixel electrode PX overhangs

over the common signal line CL beyond the unit pixel, and an overhanging distal end forms an extending portion PXJ, which is retracted-spaced from a side of the common signal line CL at a side opposite to the pixel electrode. The common electrodes CT are formed such that ~~the common electrodes CT~~ they extend in the inside of the unit pixel covering the common signal line CL, except for a portion along the extending portion PXJ of the pixel electrode PX. Due to such a constitution, an electric field E between the common signal line CL and the pixel electrode PX can be blocked.

Further, the extending portion PXJ of the pixel electrode PX has a width that is larger than a-the width of the other portion of the pixel electrode PX in the X direction, and the source electrode SD has a projecting portion SD3 which projects in a step-like manner in the direction which intersects the extending direction of the common signal line CL. The step-like projecting portion SD3 is arranged between the common signal line CL and the common electrode CT and is arranged-disposed at a position which is blocked by the common electrode CT. The step-like projecting portion SD3 is arranged above the common signal line CL and at a position where the projecting portion SD3 superposes-is superposed on an extending portion PXJ of the pixel electrode PX and is connected to the pixel electrode PX via the through hole SH.

Then, assuming a-that the distance of the step-like projecting portion SD3 of the source electrode SD from an end portion of the common signal line CL in the Y direction as-is "a", a-the distance in the X direction of the edge in the Y direction parallel to the common signal line CL contiguous with the extending portion PXJ of the pixel electrode PX as-is "b", and a-the distance in the X direction between the edge in the Y direction of the overhanging portion PXJ of the pixel electrode PX and

the common electrode CT as-is "c", the relationship  $a \geq 0$  is established. Further, the relationship  $b > c \times 2.0$  is established. Further, the relationship  $a \geq 0$  and  $b > c \times 2.0$  is established. Due to such the setting of such relationships, the electric field E between the common signal line CL and the pixel electrode PX is blocked, or the electric field E generated between the source electrode SD of the thin film transistor TFT and the common electrode CT is blocked. Further, the electric field E generated between the edge of the source electrode SD and the common signal line CL is also blocked.

According to this embodiment, a switching of the liquid crystal molecules irrelevant without regard to the normal switching operation of the unit pixel is suppressed, and, hence, no irregularities are generated with respect to a the transmitting light (or a reflection light) of the liquid crystal layer, whereby the a high-quality image display can be obtained.

~~Fig. 5 is an explanatory view of the constitution of 5A shows the vicinity of a unit pixel in the a third embodiment of a liquid crystal display device according to the present invention, wherein Fig. 5A is a plan view and Fig. 5B is a cross sectional view taken along a line A-A' in Fig. 5A. Further, Fig. 6 is an explanatory view of an essential part of 6A shows the detailed structure in the vicinity of the thin film transistor TFT in Fig. 5A.~~

In As seen in Fig. 5-5A and Fig. 6-6A, on an inner surface of the unit pixel formed on a first substrate SUB1, a plurality of scanning signal lines GL which extend in the X direction and are arranged in parallel in the Y direction; a plurality of date-data signal lines DL which extend in the Y direction and are arranged in parallel in the X direction; common signal lines CL which are disposed close to the scanning signal lines GL, and extend in the X direction, and are while being arranged in

parallel in the Y direction; a plurality of thin film transistors TFT which are arranged at intersecting portions of the scanning signal lines GL and the ~~date~~data signal lines DL; pixel electrodes PX are provided, which are driven by the thin film transistors TFT; and common electrodes CT which are connected to the common signal line CL and are arranged with respect to the pixel electrodes PX such that the common electrode CT is arranged close to the pixel electrode PX in the X direction are formed. ~~Then~~Thus, the unit pixel is formed in a region surrounded by the scanning signal lines GL and the data signal lines DL.

The pixel electrode PX is arranged in a superposed manner over the common signal line CL by way of an insulation layer (an inorganic insulation layer PAS and an organic insulation layer OPAS) and is connected to a source electrode SD of the thin film transistor TFT via a through hole SH, which penetrates the insulation layer (the inorganic insulation layer PAS and the organic insulation layer OPAS). Further, a portion of the pixel electrode PX includes an enlarged portion PXE which ~~strides~~bridges over the common signal line CL from the inside of the unit pixel. The common electrodes CT are formed such that ~~the common electrodes CT~~ they extend in the inside of the unit pixel covering the common signal line CL, except for a portion along the enlarged portion PXE of the pixel electrode PX. Due to such a constitution, an electric field between the common signal line CL and the pixel electrode PX can be blocked.

The enlarged portion PXE of the pixel electrode PX has an approximately rectangular shape, having two sides along the X direction of the unit pixel and another two sides along the Y direction, wherein the unit-pixel-side side out of the two sides along the X direction extends into and is positioned in the inside of the unit pixel beyond the common signal line CL, while the side opposite to the unit pixel out

of the two sides along the X direction is positioned inside an edge of the common signal line CL at a side opposite to the unit pixel and outside an edge of the source electrode SD at a side opposite to the unit pixel.

Then, assuming a-the distance “a” between the pixel electrode PX which extends in the Y direction from the enlarged portion PXE to the inside of the unit pixel and the common electrode CT which is arranged close to the pixel electrode PX in the X direction as-is “a”, a-the distance between two sides of the enlarged portion PXE along the X direction and the common electrodes CT which are arranged close to the enlarged portion PXE as-is “b” and a-the distance between the unit pixel-PX-side side-out of the two sides along the X direction and the common signal line CL as-is “c”, a relationship  $a>b$  is established or a relationship  $b\times 0.5 < c$  is established. Further, a-relationship relationships  $a>b$  and  $b\times 0.5 < c$  is are established. In this manner, by enlarging the pixel electrode PX, a region which can block the common signal line CL can be enlarged, whereby the electric field E between the common signal line CL and the pixel electrode PX can be blocked, or the electric field between the source electrode SD of the thin film transistor TFT and the common electrode CT can be blocked. Further, the electric field between the edge of the source electrode SD and the common signal line CL also can be also-blocked.

Fig. 7 is an explanatory view of the constitution of 7A shows the vicinity of a unit pixel in the a fourth embodiment of a liquid crystal display device according to the present invention, wherein Fig. 7A is a plan view and Fig. 7B is a cross sectional view taken along a line A-A' in Fig. 7A. Further, Fig. 8 is an explanatory view of an essential part of 8A shows the detailed structure in the vicinity of the thin film transistor TFT in Fig. 7A.

This embodiment is characterized in that portions of the common signal line CL have a-the width thereof narrowed at both sides of the projecting portion SD4 of the source electrode SD in-as compared to the embodiment 3, wherein a unit-pixel-side edge of the common signal line CL is retracted such that a-the distance between the unit-pixel-side edge of the common signal line CL and the unit-pixel-side side in the X direction of the enlarged portion PXE of the pixel electrode PX is increased. That is, in the region of a portion of the source electrode SD2 which is covered with the enlarged portion PXE of the pixel electrode PX, in the X direction of the projecting portion SD4, which projects toward the unit pixel side and also projects in a step-like manner at the unit pixel side than-beyond the common signal line CL, a unit-pixel-side edge of the common signal line CL, which is arranged at the data signal line DL side is retracted at a side opposite to the unit pixel and the relationship of the distance a > distance b is established.

According to this embodiment, in addition to the advantageous effects of the third embodiment, a switching of the liquid crystal molecules irrelevant without regard to the normal switching operation of the unit pixel can be further suppressed, and, hence, no irregularities are generated with respect to a-the transmitting light (or a reflection light) of the liquid crystal layer, whereby the-a high-quality image display can be obtained.

~~Fig. 9 is an explanatory view of the constitution of 9A shows the vicinity of a unit pixel in the-a fifth embodiment of a liquid crystal display device according to the present invention, wherein Fig. 9A is a plan view and Fig. 9B is a cross sectional view taken along a line A-A' in Fig. 9A. Further, Fig. 10 is an explanatory view of an essential part of 10A shows the detailed structure in the vicinity of the thin film~~

transistor TFT in Fig. 9A, wherein while Fig. 10A is a plan view and Fig. 10B is a cross-sectional view taken along a line B-B' in Fig. 10A.

In As seen in Fig. 9-9A and Fig. 10-10A, on an inner surface of the unit pixel formed on a first substrate SUB1, a plurality of scanning signal lines GL which extend in the X direction and are arranged in parallel in the Y direction; a plurality of date-data signal lines DL which extend in the Y direction and are arranged in parallel in the X direction; common signal lines CL which are disposed close to the scanning signal lines GL, and extend in the X direction, and are while being arranged in parallel in the Y direction; a plurality of thin film transistors TFT which are arranged at intersecting portions of the scanning signal lines GL and the date-data signal lines DL; pixel electrodes PX are provided, which are driven by the thin film transistors TFT; and common electrodes CT which are connected to the common signal line CL and are arranged with respect to the pixel electrodes PX such that the common electrode CT is arranged close to the pixel electrode PX in the X direction are formed. ThenThus, the unit pixel is formed in a region surrounded by the scanning signal lines GL and the data signal lines DL.

The pixel electrode PX is arranged in a superposed manner over the common signal line CL by way of an insulation layer (an inorganic insulation layer PAS and an organic insulation layer OPAS) and is connected to a source electrode SD of the thin film transistor TFT via a through hole SH<sub>1</sub> which penetrates the insulation layer (the inorganic insulation layer PAS and the organic insulation layer OPAS). Further, a portion of the pixel electrode PX includes an enlarged portion PXE which bridges strides over the common signal line CL from the inside of the unit pixel.

The common electrodes CT are formed such that ~~the common electrodes CT~~  
they extend in the inside of the unit pixel covering the common signal line CL, except  
for a portion along the enlarged portion PXE of the pixel electrode PX, and, hence,  
an electric field between the common signal line CL and the pixel electrode PX can  
be blocked. The enlarged portion PXE of the pixel electrode PX has two sides along  
the X direction of the unit pixel and two sides along the Y direction. The unit-pixel-  
side ~~side out of~~ of the two sides along the X direction is positioned inside a unit-pixel-  
side edge (side) of the common signal line CL, while the side ~~out of~~ of two sides  
opposite to the unit pixel PX is positioned outside the edge of the common signal line  
CL opposite to the unit pixel and outside the edge of the enlarged portion PXE of the  
pixel electrode PX opposite to the unit pixel.

A side of the source electrode SD of the thin film transistor TFT opposite to  
the unit pixel extends in the X direction close to the thin film transistor TFT to form  
one side of an extending portion SDE, while another side of the source electrode SD  
at the unit pixel side extends in the X direction parallel to one side of the extending  
portion SDE at the side opposite to the unit pixel from ~~side of another~~ of the two  
sides in the Y direction close to the thin film transistor TFT. Then, assuming a that  
the distance between another side of the extending portion SDE and the edge of the  
common signal line CL opposite to the unit pixel PX as is "a" and a the distance  
between the side opposite to the unit pixel PX ~~out of~~ of another two sides of the source  
electrode SD and the neighboring common electrode in the X direction as is "b",  
these distances are set as  $a \geq 0$  and  $b \geq 0$ .

Accordingly, as shown in Fig. 10B, a portion where a fringe electric field  $E_f$ ,  
which constitutes a strong electric field formed between the source electrode SD and  
the common signal line CL, can be completely blocked by the pixel electrode PX,

and, hence, switching of liquid crystal molecules irrelevant without regard to the normal switching operation of the unit pixel can be further suppressed, whereby no irregularities are generated with respect to a-the transmitting light (or a-reflection light) of the liquid crystal layer, whereby the-a high-quality image display can be obtained.

~~Fig. 11 is an explanatory view of the constitution of 11A shows~~ the vicinity of a unit pixel in ~~the-a~~ sixth embodiment of a liquid crystal display device according to the present invention, ~~wherein Fig. 11A is a plan view and Fig. 11B is a cross sectional view taken along a line A-A' in Fig. 11A. Further, Fig. 12 is an explanatory view of an essential part of 12A shows~~ the detailed structure in the vicinity of the thin film transistor TFT in Fig. 11A.

In As seen in Fig. 11-11A and Fig. 12-12A, on an inner surface of the unit pixel formed on a first substrate SUB1, a plurality of scanning signal lines GL which extend in the X direction and are arranged in parallel in the Y direction; a plurality of date-data signal lines DL which extend in the Y direction and are arranged in parallel in the X direction; common signal lines CL which are disposed close to the scanning signal lines GL and extend in the X direction, and are while being arranged in parallel in the Y direction, a plurality of thin film transistors TFT which are arranged at intersecting portions of the scanning signal lines GL and the date-data signal lines DL; pixel electrodes PX are provided, which are driven by the thin film transistors TFT; and common electrodes CT which are connected to the common signal line CL and are arranged with respect to the pixel electrodes PX such that the common electrode CT is arranged close to the pixel electrode PX in the X direction are formed. ThenThus, the unit pixel is formed in a region surrounded by the scanning signal lines GL and the data signal lines DL.

The pixel electrode PX is arranged in a superposed manner over the common signal line CL by way of an insulation layer (an inorganic insulation layer PAS and an organic insulation layer OPAS) and is connected to a source electrode SD of the thin film transistor TFT via a through hole SH, which penetrates the insulation layer (the inorganic insulation layer PAS and the organic insulation layer OPAS).

Further, a portion of the pixel electrode PX includes an approximately rectangular enlarged portion PXE which strides-bridges over the common signal line CL from the inside of the unit pixel, while the source electrode SD includes a projecting portion SD4 which projects toward the unit pixel side at a portion which is covered with the enlarged portion PXE of the pixel electrode PX and projects toward the unit pixel side in a step-like manner beyond the common signal line.

The common electrode CT is formed such that ~~the common electrode CT it~~ extends (overhangs) in the inside of the unit pixel, while covering the common signal line CL, except for a portion of the common signal line CL, along the enlarged portion PXE of the pixel electrode PX so as to block an electric field between the common signal line CL and the pixel electrode PX. At the same time, the common electrode CT includes common electrode enlarged portions CTE, which are enlarged to cover the common signal lines CL corresponding to the enlarged portion PXE of the pixel electrode PX at portions of the thin film transistor TFT side, while having sides which extend at an angle  $\theta$  in the X direction with respect to the Y direction and sides which extend in the Y direction. Here, an angle at which the pixel electrode PX in the unit pixel rises is also set to  $\theta$ .

The enlarged portions PXE of the pixel electrode PX include two sides along the X direction of the unit pixel and two another-other sides which extend in the Y

direction of the unit pixel, wherein the unit-pixel-side side ~~out~~ of the two sides along the X direction is positioned ~~in the more~~ inside of the unit pixel than the common signal line CL and another the other side of the two sides opposite to the unit pixel is positioned ~~in the more~~ inside than an edge the common signal line CL opposite to the unit pixel.

Here, by setting the angle  $\theta$  to  $90^\circ \leq \theta < 180^\circ$ , the pixel electrode PX and the common electrode CT are enlarged. Further, the relationship between a-the distance "a" between the side of the common electrode enlarged portion CTE which extends in the X direction and the step-like edge extending along the first direction, a-the distance "b" between the unit-pixel-side edge of the common signal line CL and the edge along the X direction of the projecting portion SD4 of the source electrode SD which projects in a step-like manner, and a-the distance "c" between the pixel electrode PX and the edge in the Y direction of the common electrode enlarged portion CTE is set to  $a > b$ , and is preferably set to  $(a-b) > c$ . Due to such a constitution, an electric field from the common signal line CL can be surely blocked, and, hence, the image retention areas are reduced, whereby the effective numerical aperture can be increased.

Here, in the above-mentioned respective embodiments, by setting the distance between the pixel electrode PX and the source electrode SD in Fig. 4-1A to be smaller than the distance between the pixel electrode PX and the common electrode CT, which are arranged to close each other, for example, a DC component which remains between the electrodes can be reduced; and, hence, the image retention can be suppressed, and, at the same time, the effective numerical aperture is enhanced. Here, as shown in Fig. 14-14A, by forming a black matrix BM such that the black matrix BM covers the longitudinal end portions of the pixel electrodes PX

and the common electrodes CT ~~in the inside~~ of the unit pixel, the a degradation of image quality attributed to the image retention can be further suppressed.

Further, in the above-mentioned respective embodiments, by further arranging the common signal lines CL along both sides of the scanning signal line GL, it is possible to ~~have an effect for blocking~~ more effectively block the leaking of the electric field from the scanning signal line GL. Here, the end portions of the common electrodes CT are formed such that the end portions are retracted from the common electrodes CT ~~in the inside~~ of the unit pixel.

Fig. 13 is a schematic cross-sectional view ~~for explaining a constitutional showing one example of the unit pixel portion and the peripheral portion of the liquid crystal display device of the present invention. In the drawing, SUB1 indicates the first substrate which is explained has been identified in conjunction with the respective embodiments. A first orientation film ORI1 is applied to an uppermost layer on the first substrate SUB1, which is brought into contact with the liquid crystal layer LC, and rubbing treatment is applied to the first orientation film. Then, a first polarizer POL1 is formed on an outer surface of the substrate SUB2. Here, the same reference symbols which are equal to the references symbols are used in the foregoing explanation correspond to identify identical functional portions in the respective embodiments, wherein PSV indicates a protective film and the constitution of the first substrate SUB1 is simplified.~~

Further, SUB2 indicates the second substrate, which constitutes the counter substrate, wherein the second substrate SUB2 includes color filters CF, which are defined by the black matrix BM, and an overcoat layer OC, which is formed above the color filters CF. Further, a second orientation film ORI2, which is brought into contact with the liquid crystal layer LC, is applied to the overcoat layer OC, and

rubbing treatment is applied to the second orientation film ORI2. A second polarizer POL2 is mounted on an outer surface (a viewing side) of the second substrate SUB2. A sealing material SL seals between peripheral portions of the first substrate SUB1 and the second substrate SUB2.

\_\_\_\_\_ The electric field EP which performs effects switching of the unit pixel (turning on/off of the unit pixel) is formed between the pixel electrode PX and the common electrode CT in the direction parallel to the respective substrate surfaces.

As has been explained heretofore, according to the present invention, the generation of the-an undesired electric field between the common signal lines and the pixel electrodes and the common electrodes in the periphery of the unit pixel, particularly in the vicinity of the thin film transistor, can be suppressed, and, hence, the occurrence of image retention can be reduced, whereby it is possible to provide the-a liquid crystal display device which is capable of displaying the-high quality images.